Docket No.: 247971US2RD/wms

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Shinobu FUJITA, et al.

SERIAL NUMBER: 10/761,326 GROUP: 2193

FILED: January 22, 2004 EXAMINER: MAI, TAN V

FOR: SEED GENERATING CIRCUIT, RANDOM NUMBER GENERATING CIRCUIT,

SEMICONDUCTOR INTEGRATED CIRCUIT, IC CARD, AND INFORMATION

TERMINAL EQUIPMENT

REQUEST TO CORRECT ALLOWED CLAIMS

MAIL STOP ISSUE FEE COMMISSIONER FOR PATENTS P.O. BOX 1450 **ALEXANDRIA, VA 22313-1450**

SIR:

In the matter of the above-identified application for patent, we hereby request correction of your records to reflect the correct allowed claims.

The allowed claims should be: 1, 5-20.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Registration No. 24,913

Customer Number

22850

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 03/06)

James H. Knebel Registration No. 22,630